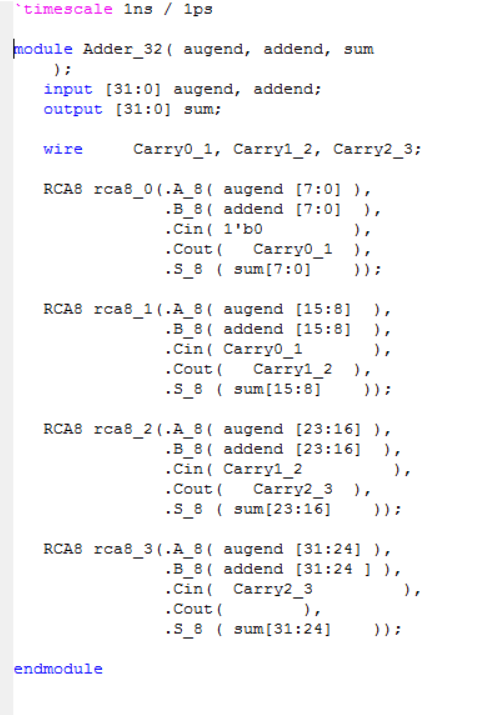
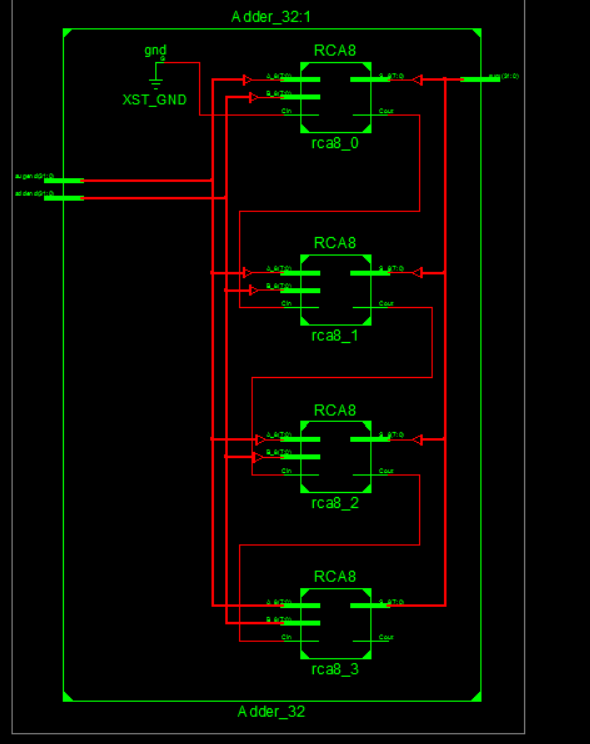
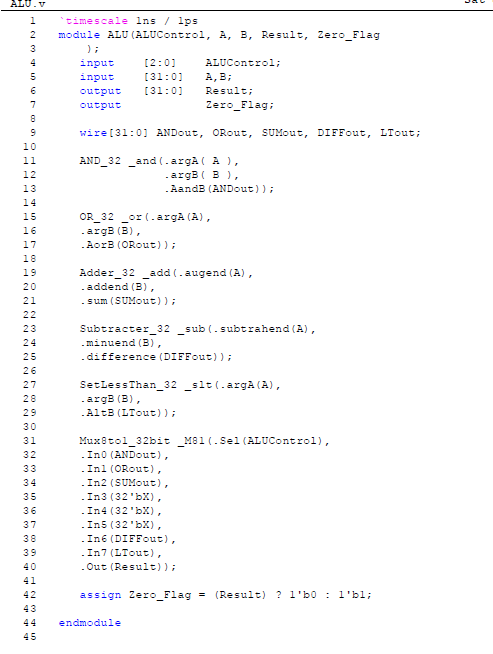
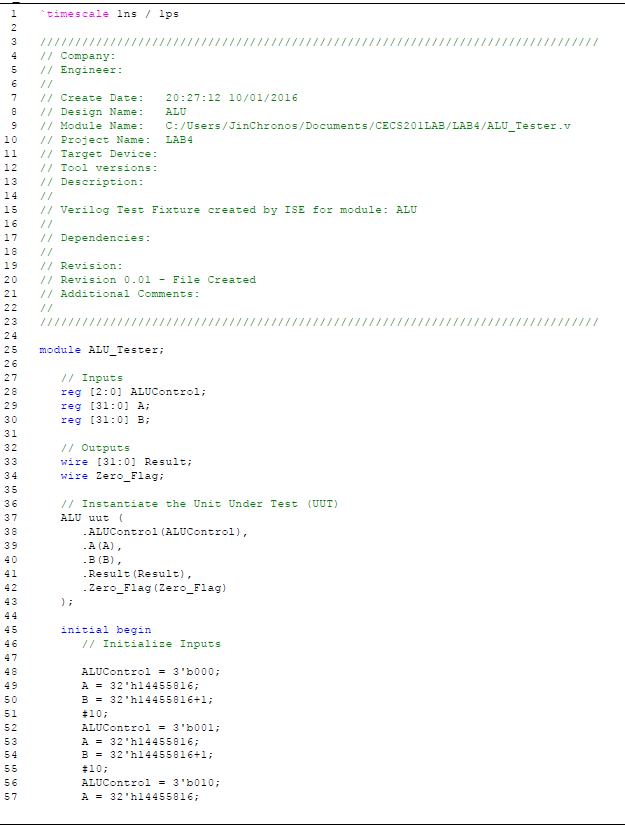
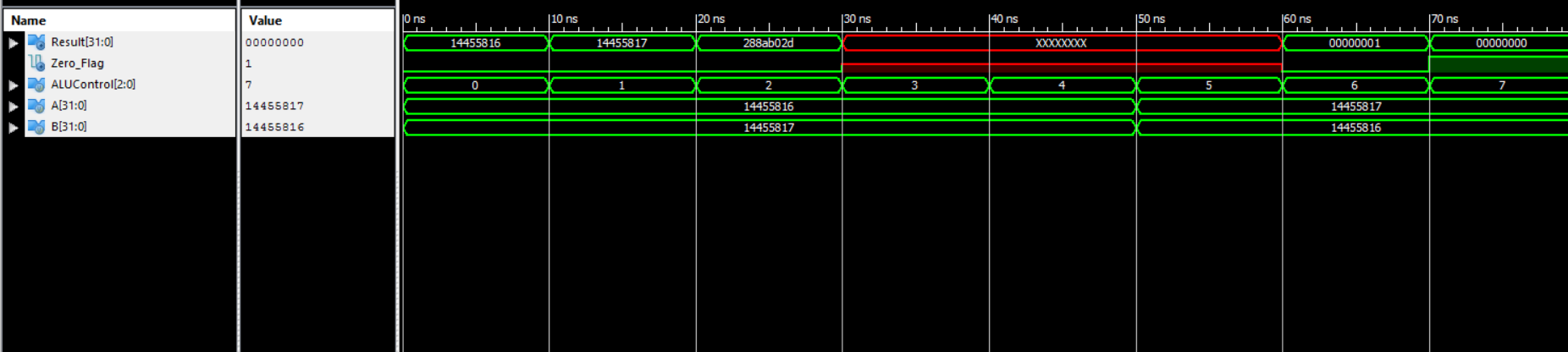
CECS 225

Lab 4

Eugene Paul Mesina



* **Section 1:** Adder\_32
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* **Section 2:** RTL Schematic of Adder\_32
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* **Section 3:** ALU
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* **Section 4:** ALU\_Tester
* **Section 5:** ALU Test Results
* 
* **Section 6:** RTL Schematics
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